

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("5740069").PN.	USPAT	OR	OFF	2006/07/18 10:22
L2	478	((programmable adj1 logic) or (programmable adj1 array) or pld or fpga) and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) and (direct\$2 NEAR1 (coupl\$3 or connect\$3)) and multiplex\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/18 10:32
L3	365	2 and ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/18 10:32
L4	4	3 and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) WITH (direct\$2 NEAR1 (coupl\$3 or connect\$3)) WITH ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/18 10:44
L5	13	3 and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) SAME (direct\$2 NEAR1 (coupl\$3 or connect\$3)) SAME ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/18 10:48

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	((((programmable adj1 logic) or (programmable adj1 array) or pld or fpga) and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) SAME (direct\$2 NEAR1 (coupl\$3 or connect\$3)) SAME ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))).clm.	US-PGPUB	OR	OFF	2006/07/18 10:59
L2	1	((((programmable adj1 logic) or (programmable adj1 array) or pld or fpga) and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) and (direct\$2 NEAR1 (coupl\$3 or connect\$3)) SAME ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))).clm.	US-PGPUB	OR	OFF	2006/07/18 11:00
L3	1	((((programmable adj1 logic) or (programmable adj1 array) or pld or fpga) and ((i/o adj1 block\$1) or (i/o adj1 circuit\$3) or (i/o adj1 cell\$1)) and (direct\$2 NEAR1 (coupl\$3 or connect\$3)) and ((logic adj1 element\$1) or (logic\$1 adj2 (block\$1 or cell\$1)) or (function adj1 generat\$3))).clm.	US-PGPUB	OR	OFF	2006/07/18 11:00